



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Matthew J. Adiletta, et al.      Art Unit : 2187  
Serial No. : 09/387,109      Examiner : David Jesse Diller  
Filed : August 31, 1999  
Title : SDRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

PETITION FOR ONE-MONTH EXTENSION OF TIME

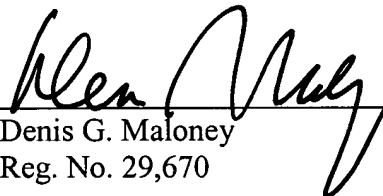
Pursuant to 37 CFR §1.136, applicant hereby petitions that the period for response to the action dated September 13, 2004, be extended for one month to and including January 13, 2005.

Enclosed is a check for \$120 for the required fee. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: \_\_\_\_\_

1/13/05

  
\_\_\_\_\_  
Denis G. Maloney  
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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Date of Deposit

January 13, 2005

\_\_\_\_\_  
Signature

Irja Zarembok

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